A ROUGH PROGRAMMING APPROACH TO POWER-AWARE VLIW INSTRUCTION SCHEDULING FOR DIGITAL SIGNAL PROCESSORS

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ABSTRACT

Current techniques for power-aware VLIW instruction scheduling assumed that the power consumption parameters are precisely known. In reality, there will always be some degree of imprecision. In this paper, we propose to apply rough set theory to handle the imprecision involved. Power consumption parameters are modeled as rough variables and the power-balanced instruction scheduling problem is formulated as a rough program. The effectiveness and advantages of our approach is illustrated through examples.

1. INTRODUCTION

Advanced digital signal processors employ VLIW architectures for demanding signal processing applications. Each long instruction word consists of one or more instructions that can be executed in parallel on different functional units. These processors rely on the compiler to schedule instruction at compile time to meet deadline as well as power constraints. Average power consumption reduction is known to be an important constraint for its great impact on battery life and heat dissipation. Significant processor supply current variations cause power supply noise, degrade chip reliability and accelerate battery exhaustion. Hence power variation reduction without compromising execution speed is another important instruction scheduling constraint in embedded VLIW systems.

Power-aware instruction scheduling refers to the task of producing a schedule of these parallel instructions so that the average power consumption is minimized or the power variation over the execution of the program is minimized, while the deadline constraints are met. Previously published works in this area make use of power consumption models with parameters that are assumed to be precisely known [1, 2, 4, 5]. However, in reality, the values of these parameters are not precise for two main reasons. Firstly, physical measurements, which has been an important approach to instruction-level power modelling and estimation for microprocessors [6-9], are always imprecise. The variations in the measured values are using handled by using the mean or median of a large number of measurements. Secondly, in order to reduce the complexity of the power model, those instruction with consume similar amounts of power are typically clustered together and given a the same power figure [3]. While these approximations allow us to optimize power consumption in the average sense, we are not able to get any idea of the deviations from the average that may actually occur.

There are several approaches to deal with imprecision or uncertainty. In this paper, we propose to use the rough set theory [10] approach to model the uncertainty inherent in the power model parameters. The instruction scheduling problem can then be formulated as a rough program [11]. One of the main advantages of rough set is that it does not need any prior information on the data, such as probability distributions in statistics, basic probability assignment in the Dempster-Shafer theory [12], or grade of membership in fuzzy set theory [13].

This paper focuses on the optimization problem of VLIW instruction scheduling for power variation reduction and is an extension of our previous work [5]. The rest of the paper is organized as follows. Section 2 introduces a simple power model for VLIW architectures. The mixed integer programming formulation for this optimization problem is described in Section 3. Section 4 presents the method for modelling the power consumption parameters as rough variables based on measurements. Section 5 proposes a rough programming formulation for the optimization problem of VLIW instruction scheduling for balanced power consumption. Throughout this paper, we assume that an initial instruction schedule that meets the speed performance requirements has been obtained through list or modulo scheduling [14]. Our algorithm reschedules instructions for power variation optimization in the second phase.

2. POWER MODEL

A VLIW processor with an issue width of k can execute at most k instructions simultaneously on separate functional units. Each instruction requires a different amount of time to execute. We divide the time line into equal length time slots. A power cost p_i is associated with each instruction i which represents the average power consumed by this instruction over the instruction execution.

Let the instruction schedule be $N = \langle N_1, N_2, ..., N_t \rangle$ where $N_i = (n_1, n_2, ..., n_k)$ is the very long instruction word issued at the *i*-th time slot of N. The power consumption at the *i*-th time slot of schedule N is the sum of the power consumed by all the executing instructions, issued either at the *i*-th time slot or previous ones. This can be expressed mathematically as

$$P^{i} = \sum_{1 \le k \le i} \sum_{n_{j} \in N_{(i-k+1)}} p_{n_{j}}$$
(1)

where n_j is an executing instruction at the *i*-th time slot. Thus the average power consumption over all t time slots is given by

$$M = \left(\sum_{i=1}^{t} P^{i}\right)/t \tag{2}$$

The power deviation from the average value at any given time slot i is

$$PV^i = |P^i - M| \tag{3}$$

Therefore, the total power deviation for a schedule is given by

$$PV = \sum_{i=1}^{t} PV^i \tag{4}$$

This is a rather simplified power model for the VLIW instructions. However, our techniques do not depend on a particular power model. It can be easily be modified to work with more sophisticated power models if they are available.

3. MIXED-INTEGER PROGRAM FORMULATION

The conventional mixed-integer program for the scheduling of VLIW instructions for minimal power variations is given by P1 and efficient techniques have been proposed to solve it [5].

P1: min
$$f(X,\xi)$$
 subject to

subject to

$$X = \bigcup x_i^k \quad i = 1, ..., n; k = 1, ..., t$$

$$x_i^k \in \{0, 1\} \quad i = 1, ..., n; k = 1, ..., t$$
 (5)

$$G(X) \le 0$$

$$L(X) = 0$$
(6)

where $f(X,\xi)$ is the power variation of a given schedule X over time which we seek to minimize, and ξ denotes the set of the power consumption parameters. In (5), n is the number of instructions in X and t is the number of time slots available. The binary decision variables x_i^k has a value of 1 if instruction *i* is rescheduled in time slot k; otherwise its value is zero. $G(X) \leq 0$ and L(X) = 0in (6) denote the constraint matrix for processor-specific resource constraints, data dependence constraints and performance deadline constraints.

The basic problem with the above formulation is that the power consumption parameters ξ in the objective function need to be precise values.

4. ROUGH VARIABLE REPRESENTATION OF POWER CONSUMPTION PARAMETERS

The imprecision of the power consumption parameters can be encapsulated by expressing them as rough variables. Based on [11] we shall define rough space and rough variables.

Definition 4.1 Let Λ be a nonempty set, \overline{A} a σ -algebra of subsets of Λ , Δ an element in \overline{A} , and π a set function satisfying the following axioms:

- 1. π {A} \geq 0 for any $A \in \overline{A}$.
- 2. For every countable sequence of mutually disjoint events $\infty = \infty$

$$\{A_i\}_{i=1}^{\infty}$$
, we have $\pi\{\bigcup_{i=1}^{\infty} A_i\} = \sum_{i=1}^{\infty} \pi\{A_i\}.$

Then $(\Lambda, \Delta, \overline{A}, \pi)$ is called a rough space.

Definition 4.2 A rough variable ζ is a function from the rough space $(\Lambda, \Delta, A, \pi)$ to the set of real numbers such that for every *Borel set* B *of* \Re *, we have* $\{\lambda \in \Lambda | \xi(\lambda) \in B\} \in A$ *.*

Definition 4.3 The lower and the upper approximations of the rough variable ζ are then defined as $\zeta = \{\zeta(\lambda) | \lambda \in \Delta\}$ and $\overline{\zeta} = \{\zeta(\lambda) | \lambda \in \Lambda\}$ respectively.



Fig. 1. Data dependency graph for instructions in Example 4.1

A power consumption parameter p_i can be formulated as a rough variable ([a, b], [c, d]) with $c \leq a \leq b \leq d$ on the real line where [a, b] is the lower approximation and [c, d] is the upper approximation. This means that the power consumption values within [a, b] is sure and that within [c, d] is possible. We can compute these approximations for each $p_i \in \xi$ based on experimental data in three steps:

1) Measurements: Conduct repeated measurements for each $p_i \in \xi$ and collect the data. Principles for design of experiments can be applied to reduce the impact of nuisance factors [15].

2) Discretization: Based on the obtained measurement data, discretize the power consumption values on the real line according an "equal" relation defined as follows. Let S(x, y) be the measure that quantifies the closeness between two power consumption values x and y. Then, x and y are similar when $S(x, y) \ge h$, where $h \in [0,1]$ is a similarity threshold value. The "equal" relation R based on this similarity measure is defined as

$$xRy \Leftrightarrow S(x,y) \ge h$$

In determining the optimal similarity threshold value h, we need to balance the requirements that 1) some "equal" measurement data are required to be categorized into the same p_i as many as possible and 2) some measurement data categorized into the same p_i are required to be "equal" as much as possible. After finding the optimal similarity threshold value, the power consumption values in real line are grouped into granules according to the "equal" relation.

3) Lower/upper approximations: For each $p_i \in \xi$, compute the lower and upper approximations according to the Definition 4.3.

Example 4.1 Consider the TMS320C6711 [16] which is a VLIW digital signal processor. An initial performance optimized instruction schedule X_1 consisting of fourteen instructions is given by

$$X_1 = \{x_1^1, x_2^1, x_3^1, x_4^1, x_5^2, x_6^2, x_7^2, x_8^3, x_9^4, x_{10}^5, x_{11}^5, x_{12}^5, x_{13}^6, x_{14}^6\}$$

where the superscripts indicate the time slot in which the instruction is being scheduled. These fourteen instructions are { addaw, add, addaw, add, ldw, mv, addaw, stw, b, addaw, cmpeq, stw, ldw, b }. The data dependence graph as shown in Fig. 1.

To represent the set of power consumption parameters $\xi =$ $\{p_{addaw}, p_{add}, p_{ldw}, p_{mv}, p_{stw}, p_b, p_{cmpeq}\}$ as rough variables, we randomly conducted fifty repeated measurements for each parameter. Table 1 shows the data set for power parameter p_{addaw}

Table 1. Current readings(mA) of 50 measurements for p_{addaw}									
20)6	208	203	197	208	210	204	202	203
19	91	191	204	203	201	194	212	211	199
20)5	194	210	209	198	212	196	197	194
19	97	203	201	212	207	200	203	205	203
20)3	200	190	196	206	196	206	205	204
20)5	196	198	195	202				

Table 2. Discretized current readings of measurements for p_{addaw}

[203,207)	[207,214)	[203,217)	[190,198)	[207,214)
[207,214]	[203,207)	[202,203)	[203,207)	[190,198)
[190,198)	[203,207)	[203,207)	[198,202)	[190,198)
[207,214]	[207,214)	[198,202)	[203,207)	[190,198)
[207,214]	[207,214)	[198,202)	[207,214)	[190,198)
[190,198)	[190,198)	[190,198)	[203,207)	[198,202)
[207,214]	[207,214)	[198,202)	[203,207)	[203,207)
[203,207)	[203,207)	[198,202)	[190,198)	[190,198)
[203,207)	[190,198)	[203,207)	[203,207)	[203,207)
[203,207)	[190,198)	[198,202)	[190,198)	[202,203)

consisting of 50 repeated measurements. Based the measured data, the possible power consumption values on real line are discretized and the lower and upper approximations for each power consumption parameter are generated using the Rosetta Toolkit [17]. A simple similarity measure since the current reading is the only numerical attribute. It is given by

$$S(x,y) = d(x_{current}, y_{current}) = |x_{current} - y_{current}| \quad (7)$$

where $x_{current}$ and $y_{current}$ are the current readings of two measurements x and y. The partial discretization results corresponding to Table 1 are shown in Table 2. The categorization rules are shown in Table 4. According to these categorization rules, the lower and upper approximations for each parameter are obtained. They are shown in Table 3.

5. ROUGH PROGRAMMING FORMULATION

If ξ is a set of rough variables, then the values of the function $f(X,\xi)$ for any given X are also rough variables. The rough returns of $f(X,\xi)$ may be ranked by 1) the expected value $E[f(X,\xi)]$; 2) the α -optimistic value $f(X,\xi)_{sup}(\alpha)$ or the α -pessimistic value $f(X,\xi)_{inf}(\alpha)$, for some predetermined confidence level $\alpha \in (0,1]$; 3) the trust measure $Tr\{f(X,\xi) > \overline{r}\}$ for some predetermined level \overline{r} . Based on the general framework of rough chance-constrained programming (CCP) [11], we measure the rough return $f(X,\xi)$ for any decision X by its α -pessimistic value.

Definition 5.1 *let* ϑ *be a rough variable, and* $\alpha \in (0, 1]$ *. Then*

$$\vartheta_{inf}(\alpha) = \inf\{r | Tr\{\vartheta \le r\} \ge \alpha\}$$
(8)

is called the α -pessimistic value to ϑ .

Table 3. Rough power consumption parameters in Example 4.1.

$p_{addaw}, p_{add}, p_{mv}, p_{cmpeq}$	p_{ldw}, p_{stw}	p_b
$(\emptyset, [190, 214])$	$(\emptyset, [214, 233])$	$(\emptyset, [190, 207])$

We specify the trust measure operator Tr in (8) as follows.

Definition 5.2 Let $\vartheta = ([a,b], [c,d])$ be a rough variable with $c \le a < b \le d$. Let r be a given value. We then have

$$Tr\{\vartheta \le r\} = \begin{cases} 0, \ if \ c \ge r \\ \frac{c}{2(c-d)}, \ if \ a \ge r \ge c \\ \frac{2ac-ad-bc}{2(b-a)(d-c)}, \ if \ b \ge r \ge a \\ \frac{d-2c}{2(d-c)}, \ if \ d \ge r \ge b \\ 1, \ if \ r \ge d \end{cases}$$

 $f(X,\xi)_{inf}(\alpha)$ is the smallest value \overline{f} satisfying $Tr\{f(X,\xi) \leq \overline{f}\} \geq \alpha$. This means that, for a given X, the rough return of $f(X,\xi)$ will be below the pessimistic value \overline{f} with a confidence level of α .

We can now formulate the VLIW power-balanced instruction scheduling problem as a rough CPP. Solving this program involves searching for the minimum α -pessimistic value $f(X, \xi)_{inf}(\alpha)$ among all feasible schedules X.

P2: min $f(X,\xi)_{inf}(\alpha)$ subject to

$$f(X,\xi)_{inf}(\alpha) = \inf\{\overline{f}|Tr\{f(X,\xi) \le \overline{f}\} \ge \alpha\}$$
(9)

$$X = \bigcup x_i^k \quad i = 1, ..., n; k = 1, ..., t$$

$$x_i^k \in \{0, 1\} \quad i = 1, ..., n; k = 1, ..., t$$
 (10)

$$G(X) \le 0$$

$$L(X) = 0$$
(11)

where α is the specified confidence level and ξ is the set of rough power consumption parameters. (10) and (11) are the same as those in the conventional formulation since there are no rough variables involved. The optimal solution obtained from this formulation is the schedule with the optimal α -pessimistic value of the objective function $f(X, \xi)$.

Example 5.1 Continuing from Example 4.1, suppose the confidence level is $\alpha = 0.9$. We have the following rough CPP model for the scheduling problem:

min $f(X,\xi)_{inf}(0.9)$ subject to

$$f(X,\xi)_{inf}(0.9) = inf\{\overline{f}|Tr\{f(X,\xi) \le \overline{f}\} \ge 0.9\}$$
 (12)

$$f(X,\xi) = \sum_{k=1}^{6} |P^{k} - M|$$
$$M = \left(\sum_{k=1}^{6} P^{k}\right) / 6$$
(13)

$$P^{k} = \sum_{i=1}^{14} x_{i}^{k} p_{i} + \sum_{i=1}^{14} x_{i}^{k-1} \varepsilon(D_{i} - 1) p_{i}$$

$$\xi = \cup p_i \quad i = 1, ..., 14 \tag{14}$$

$$X = \bigcup x_i^{k} \quad i = 1, ..., 14; k = 1, ..., 6$$

$$x_i^{k} \in \{0, 1\} \quad i = 1, \dots, 14; k = 1, \dots, 6$$

$$(15)$$

$$G(X) \le 0$$

$$L(X) = 0$$
(16)

The rough power consumption parameters in (14) are given in Table 3. In (13), D_i is the delay slots of instruction *i* and $\varepsilon(x)$ is defined by

$$\varepsilon(x) = \begin{cases} 1 & if \ x \ge 1 \\ 0 & otherwise \end{cases}$$

 Table 4. Categorization rules for each power consumption parameter in Example 4.1.

$current([203, 207)) = parameter(p_{add}) \text{ OR } parameter(p_b) \text{ OR } parameter(p_{addaw}) \text{ OR } parameter(p_{mv}) \text{ OR } parameter(p_{cmpeq})$
$current([207, 214)) => parameter(p_{add}) OR parameter(p_{addaw}) OR parameter(p_{mv}) OR parameter(p_{cmpeq})$
$current([190, 198)) = parameter(p_{add}) \text{ OR } parameter(p_b) \text{ OR } parameter(p_{addaw}) \text{ OR } parameter(p_{mv}) \text{ OR } parameter(p_{cmpeq})$
$current([202, 203)) = parameter(p_{add}) \text{ OR } parameter(p_b) \text{ OR } parameter(p_{addaw}) \text{ OR } parameter(p_{mv}) \text{ OR } parameter(p_{cmpeq})$
$current([198, 202)) = parameter(p_{add}) OR parameter(p_b) OR parameter(p_{addaw}) OR parameter(p_{mv}) OR parameter(p_{cmpeq})$
$\operatorname{current}([214, 234)) => \operatorname{parameter}(p_{ldw}) \operatorname{OR} \operatorname{parameter}(p_{stw})$

where x is an integer. $G(X) \le 0$ and L(X) = 0 in (16) denote the constraint matrix for processor-specific resource constraints, data dependence constraints and performance deadline constraints respectively.

We use a hybrid intelligent algorithm [11] to solve the rough program. The optimal schedule obtained is

$$X_{op} = \{x_1^1, x_2^1, x_3^4, x_4^1, x_5^2, x_6^4, x_7^1, x_8^3, x_9^4, x_{10}^5, x_{11}^5, x_{12}^5, x_{13}^6, x_{14}^6\}$$

The objective function $f(X, \xi)$ has an optimal 0.9-pessimistic value of 127. That is,

$$\inf\{\overline{f}|Tr\{f(Xop,\xi) \le \overline{f}\} \ge 0.9\} = 127 \tag{17}$$

The results obtained in this example can be compared to one obtained using the mixed-integer formulation in Section 3. In this case, the median values of the measured data are used for the power consumption parameters. The resulting optimal schedule is also X_{op} as given in Example 5.1, but the optimal objective function value is 121.

According to (17), the optimal power variation, which may actually occur, is less than or equal to 127 with a confidence level above 0.9. Therefore, the result obtained from the mixed integer program of 121 falls within the range obtained by rough programming. Hence it validates the rough programming result. In addition, the main advantage of the rough programming result is that it indicates the deviations which can be expected.

6. CONCLUSIONS

Rough set theory has been applied to the problem of power-balanced VLIW instruction scheduling. We showed how the ideas from rough set theory can be used to model the imprecise power consumption parameters as rough variables and formulated the scheduling problem as a rough program. The advantages and effectiveness of our approach has been demonstrated through examples. This work is a first attempt to apply rough set theory to this area. Future work involves the development of more efficient algorithms to solve the rough programming model by exploiting the problem specific structure.

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